

**EVERYTHING TEST**

# Digital Design & Test Seminar

Learn the best design engineering measurement techniques for signal & power integrity, EMI, and more...

## Please Join Us

Digitalization is everywhere and with this the demand for increasing processing power and data rates. This applies for data centers and AI, but also for many other areas like autonomous driving, industry automation, aerospace & defense and wireless communication. But what does this mean for development and testing?

This seminar will explore major trends and the evolution of the related technologies and will discuss the impact on high-speed designs and their power distribution networks as well as the increasing challenges in signal integrity and power integrity. Join us on these important topics!

### What will you learn?

- ▶ Industry trends & related technologies
- ▶ Challenges, solutions, & best practices in Signal Integrity and Power Integrity Test
- ▶ How to debug & optimize high speed interfaces
- ▶ Insights on channel characterization & de-embedding
- ▶ Tips on power integrity in static conditions & dynamic load step scenarios
- ▶ Latest on advanced & effective tools to troubleshoot your design effectively
- ▶ And more...

## Course Agenda

### 9:00 AM — 12:00 PM

- ▶ Global digitalization, big data and AI: drivers, trends, opportunities & new design challenges
- ▶ Power Design test for high-speed applications
- ▶ Signal Integrity Analysis- Eye Diagrams, Jitter & More

### 12:00 PM — 1:00 PM

- ▶ Lunch, Provided by Rohde & Schwarz

### 1:00 PM — 3:00 PM

- ▶ Characterizing the High-Speed Transmission Channel
- ▶ Additional demonstrations & networking

### Date:

Thursday, April 17

### Time:

9:00am to 3:00pm

### Location:

Riverside Hotel  
2900 Chinden Boulevard  
Cinnabar Room  
Boise, ID 83714

### Registration Link:

<https://qrco.de/25BoiseDigital>

This seminar is complimentary with lunch provided. Please arrive early for registration & seating.

We look forward to seeing you there!

SCAN TO REGISTER

